

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bernd Deutschmann, et al. Art Unit : 2836
Serial No. : 10/588,984 Examiner : Dharti H. Patel
Filed : November 21, 2006 Confirmation No.: 4272
Title : CIRCUIT ARRANGEMENT AND METHOD FOR PROTECTING AN
INTEGRATED SEMICONDUCTOR CIRCUIT Notice of Allowance Date: February 8, 2010

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

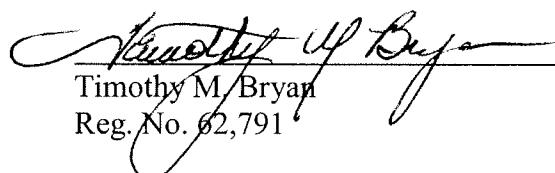
RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed February 8, 2010, enclosed is a completed issue fee transmittal form PTOL-85b.

The issue fee and publication fee in the amount of \$1810 is being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply all charges or credits to Deposit Account No. 06-1050, referencing Attorney Docket No. 14603-0026US1.

Respectfully submitted,

Date: May 2, 2010


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